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	Application No.	Applicant(s)	
Notice of Allowability	10/615,777	HIDAKA, HIDETO	
Nouce of Allowability	Examiner	Art Unit	
	ANH PHUNG	2824	
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	oplication. If not includ n will be mailed in due	led course. THIS
1. This communication is responsive to the application filed of	on July 10, 2003		
2. ☑ The allowed claim(s) is/are <u>1-11</u> .			
3. \square The drawings filed on <u>10 July 2003</u> are accepted by the Ex	kaminer.		
 4. Acknowledgment is made of a claim for foreign priority unance and all b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of he priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE 	e been received. e been received in Application No. cuments have been received in this of this communication to file a reply		
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			OTICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date	son's Patent Drawing Review(PTG 's Amendment / Comment or in the (1.84(c)) should be written on the drawi	Office action of ings in the front (not the	e back) of
 DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT 			ote the
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date July 10, 2004 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	8. ⊠ Examiner's Stateme 9. ⊠ Other <u>Search Histor</u>	(PTO-413), te ment/Comment ent of Reasons for Allo	
	ANH PI PRIMARY E	HUNG 🗸	

Detailed Action

1. In response to the U.S. Application Serial No. 10/615,777 filed on July 10, 2003, claims **1-11** are pending in the application.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

This office acknowledges receipt of the following items from the Applicant:
 Information Disclosure Statement (IDS) filed July 10, 2003.
 Information disclosed and listed on PTO 1449 was considered.

Examiner's Statement of Reasons for Allowance

4. Claims **1-11** are allowed.

Ooishi (US 6,714,443), Nahas (US 6,714,442), Gogl et al. (US 6,577,528), Swanson et al. (US 6,493,259) and Perner et al. (US 6,256,224) disclose a magnetic memory device having bit line drive circuits similar to that of the present application, but fail to teach:

"a plurality of bit line drive circuits disposed corresponding to the bit lines and supplying currents according to write data to a corresponding bit line, each bit line drive circuit including; a first drive circuit for supplying a first current to a corresponding bit line in accordance with write data to an adjacent column when the adjacent column is selected; and a second drive circuit for supplying a second current to the corresponding

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bit line in accordance with the write data to the corresponding column when the corresponding column is selected" as claimed in the independent claim 1; or

"a plurality of bit lines, disposed corresponding to the columns of said plurality of magnetic memory cells, each connecting to the memory cells of a corresponding column; a column selecting circuit for selecting a predetermined number of memory cell columns in parallel from the columns of said plurality of magnetic memory cell in accordance with an address signal, said any adjacent columns in said predetermined number of memory cell columns sandwiching at least one bit line; and a plurality of bit line drive circuits disposed corresponding to the bit lines and supplying a current to a corresponding bit line in accordance with write data and a column selection signal from said column selecting circuit" as claimed in the independent claim 9.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comment on Statement of Reasons for Allowance".

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-1883**. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD ELMS**, can be reached on **(571) 272-1869**. The fax phone

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number for the organization where this application or proceeding is assigned is **(703) 872-9306**.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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PRIMARY EXAMINER